



Docket No.: M4065.0901/P901
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Hagop A. Nazarian

Confirmation No.: 3940

Application No.: 10/699,652

Allowed: September 22, 2005

Filed: November 4, 2003

Group Art Unit: 2827

For: SERIAL TRANSISTOR-CELL ARRAY
ARCHITECTURE

Examiner: Tan Nguyen

**APPLICANT'S COMMENTS ON EXAMINER'S STATEMENT OF REASONS FOR
ALLOWANCE**

Commissioner for Patents
Washington, DC 20231

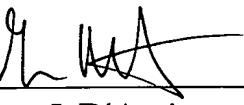
Dear Sir:

Applicant agrees in part with the Examiner's statement of reasons for allowance, found in the September 22, 2005 Notice of Allowability, in that the prior art of record fails to disclose, teach or suggest the claimed inventions. However, Applicant notes that the Examiner's statement fails to address all limitations of all of the allowed claims. For example, certain claim limitations were not discussed in the Examiner's Reasons for Allowance, including those in most of the allowed claims, each of which defines a unique combination of features not shown or suggested by the prior art, providing additional reasons for allowance of each claim.

Furthermore, Applicant respectfully submits that the scope of the claimed invention should not be limited by the Examiner's statement, but rather the scope of the claimed invention should be based on the actual language of the allowed claims.

Dated: December 19, 2005

Respectfully submitted,

By 
Thomas J. D'Amico

Registration No.: 28,371
Gianni Minutoli
Registration No.: 41,198
DICKSTEIN SHAPIRO MORIN &
OSHINSKY LLP
2101 L Street NW
Washington, DC 20037-1526
(202) 785-9700
Attorneys for Applicant